

<b>FORM PTO-1449 (SUBSTITUTE)</b>  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE          STATEMENT BY APPLICANT</b> (37 CFR 1.98(b))				Attorney Docket No.: L&L-I0225 Appl. No.: <span style="border: 1px solid black; padding: 2px;">10/730619</span>  Applicant: BURKHARD BECKER  Filing Date: December 8, 2003 Group Art Unit:			
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
YC <span style="border: 1px solid black; padding: 1px;">Y.C.</span>	A	5,539,780	7/23/96	Dutkiewicz			
YC	B	5,905,757	5/18/99	Kundmann et al.			
YC	C	6,195,782 B1	2/27/01	Rahmatullah et al.			
YC	D	5,537,445	7/16/96	Blaker et al.			
	E						
	F						
	G						
	H						
	I						
<b>FOREIGN PATENT DOCUMENT</b>							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES   NO
YC	J	0 899 887 A2	3/3/99	Europe			
YC	K	0 590 597 A2	4/6/94	Europe			
	L						
	M						
	N						
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
		Tietze, U. et al.: "Halbleiterschaltungstechnik, 11.2.2 RAM als Schieberegister", Springer Verlag, Vol. 10, 1993, pp. 284-285 and English translation "Electronic Circuits, 11.2.2 RAMs as Shift Registers", Springer Verlag, Vol. 9, 1991, pp. 284-285;					
EXAMINER /Yong Choe/				DATE CONSIDERED 12/10/2006			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							